# On Identifying in Polynomial Time Violated Subtour Elimination and Precedence Forcing Constraints for the Sequential Ordering Problem

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## **Abstract**

In this paper we consider the so-called Sequential Ordering Problem (SOP) introduced in [4] that has a broad range of applications, mainly, in production planning for manufacturing systems. Given a set of nodes, the SOP consists of finding a Hamiltonian path, such that precedence relationships among the nodes are satisfied and a given linear function is minimized. In [4] an efficient inexact algorithm for obtaining feasible solutions is described. Here, we present a strong formulation of the problem and procedures for identifying subtour elimination constraints and precedence forcing constraints that are most violated by the optimal solution of a LP relaxation of the original problem. The complexity of these separation procedures is  $O(n^4)$  and  $O(rn^3)$ , respectively, where n denotes the number of nodes and r is the number of precedence relationships.

# 1. Problem Definition

Let  $\mathscr{H}$  denote a Hamiltonian path through a given set of nodes, say V. Let  $i \to j$  mean that node i is immediately ordered before node j in a given  $\mathscr{H}$ . Let the acyclic directed graph (or digraph) P = (V,R), where R is the set of directed arcs, such that  $(ij) \in R$  means that node i has to be ordered (immediately or not) before node j in any feasible  $\mathscr{H}$ . We clearly may assume that P is transitively closed.

Let the complete digraph  $D_n = (V, A_n)$  and the matrix  $C = \{c_{i,j}\}$  be such that  $c_{i,j}$  for  $(i,j) \in A_n$  gives the cost associated with  $i \to j$ . Define

$$\overrightarrow{R} := \{(j,i) | (i,j) \in R\} 
\overrightarrow{R} := \{(i,k) \mid \exists j \text{ s.t. } (i,j),(j,k) \in R\}$$
(1.1)

and let us define the digraph D = (V,A) by setting

$$A:=A_n\setminus (\stackrel{\rightarrow}{R} \cup \stackrel{\leftarrow}{R})$$
 (1.2)

It is obvious that no feasible  $\mathscr{H}$  contains an arc from  $R \cup R$ . The Sequential Ordering Problem (SOP) consists of finding a Hamiltonian path  $\mathscr{H}$  with minimum weight in digraph D, such that the precedence relationships given by R are not violated.

The SOP has a broad application field. An obvious application is the ATSP with fixed city-origin and city-destination. The sequencing of the cities may require to satisfy some precedence relationships. Another typical application [6] is the sequencing of the operations' execution, mainly in scheduling manufacturing systems. See in [18] an interesting application and an inexact algorithm for the related STSP with precedence relationships.

The paper is organized as follows. Section 2 presents our favorite 0-1 model for the SOP. It also outlines the procedure for obtaining an optimal solution. It is based on an iterative tightening of the relaxations of the Subtour Elimination Constraints (SECs) and Precedence Forcing Constraints (PFCs). Sections 3 and 4 are devoted to the procedures for identifying violated SECs and PFCs, respectively. These procedures have complexity  $O(n^4)$  and  $O(rn^3)$ , respectively, where  $n \equiv |V|$  and  $r \equiv |R|$ . Finally, we offer some conclusions and outline future work.

# 2. The 0-1 Model

Let  $x_{i,j}$  be a 0-1 variable such that  $x_{i,j} = 1$  means that  $i \to j$  (in a feasible  $\mathscr{H}$ ) and, otherwise, it is zero. Since  $x_{j,i} = 0$  must be satisfied for all  $(j,i) \in R \cup R$  we can drop these variables. Incidentally, a key step in any practical implementation of this approach is the preprocessing procedure for reducing the cardinality of set A; i.e., arc (i,j) should be deleted from A if it has been detected that  $i \not \to j$  in any feasible  $\mathscr{H}$ , or in any better solution than the incumbent one (if any). An efficient algorithm for the SOP preprocessing is described in [4].

There are several 0-1 models for the SOP; see, e.g., [1,4]. Our favorite 0-1 model is as follows

$$\min c^t x \tag{2.1}$$

subject to

$$x(A) = n - 1 \tag{2.2}$$

$$x(\delta^-(v)) \le 1 \text{ for all } v \in V$$
 (2.3)

$$x(\delta^+(v)) \le 1 \text{ for all } v \in V$$
 (2.4)

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$$x(\delta^+(v)) \le 1 \text{ for all } v \in V$$
 (2.4)

$$0 \le x_{i,j} \le 1 \text{ for all } (i,j) \in A$$
 (2.5)

$$0 \le x_{i,j} \le 1 \text{ for all } (i,j) \in A$$

$$x(A(W)) \le |W| - 1 \text{ for all } W \subset V, 2 \le |W| \le n - 1$$
 (2.6)

$$x_{i,j} \in \{0;1\} \text{ for all } (i,j) \in A$$
 (2.7)

$$x((j:W)) + x(A(W)) + x((W:i)) \le |W|$$
for all  $(j,i) \in \overline{R}$  and all  $W \subset V \setminus \{i,j\}, W \neq \emptyset$  (2.8)

where

$$x(F) = \sum_{(i,j) \in F} x_{i,j}, \quad \delta^{-}(v) = \{(w,v) \in A\}, \quad \delta^{+}(v) = \{(v,w) \in A\},$$
$$A(W) = \{(i,j) \in A | i,j \in W\}, \quad (j:W) = \{(j,w) \in A | w \in W\} \text{ and } (W:i) = \{(w,i) \in A | w \in W.$$

We restrict c in (2.1) to the coordinates  $A \subseteq A_n$ . Constraint (2.2) defines the number of arcs in any  $\mathscr{H}$ . Constraints (2.3) (resp., (2.4)) prevent that more than one node is sequenced immediately before (resp., after) any other node. Constraints (2.6) are the Subtour Elimination Constraints SECs (see e.g. [14]). Constraints (2.8) are the Precedence Forcing Constraints PFCs.

Note that (2.1)-(2.7) can be easily converted into the classical Asymmetric Traveling Salesman Problem (ATSP). Let the AP-like LP problem (2.1)-(2.5) be named LPAP.

The basic methodology of the exact algorithm that we are using for solving (2.1)-(2.8) has the following main steps: (1) Obtaining an initial feasible solution. The inexact algorithm described in [4,5] can be used. (2) Optimizing a relaxation of the original problem. We start with LPAP. (3) Reduced cost based variable fixing coupled with an implication fixing analysis. We use the preprocessing procedure described in [4]. (4) Cutting plane generation by identification of a most violated SEC (Section 3) and PFC (Section 4). We also consider (see [1,10,12]) some types of lifted SEC's and PFC's. (5) The new constraints are added, and non-active constraints previously appended are deleted from the current LP relaxation. (6) The dual-based optimization of the new problem is performed. (7) If there is not any violated constraint except (2.7), a branch-and-cut phase is executed. We should emphasize the synergetic effect of combining the procedures for obtaining initial feasible solutions, performing reduced cost fixing and implications, and generating violated constraints. The overall framework of this methodology has been previously described and its computational results have been extensively analyzed in [3,6,11,13,14,16,17] among others. Elsewhere [1] we report

our provisional computational experience on some real-life problems. In [8] a different approach for getting strong lower bounds for the ATSP imbedded in the SOP is described. See related work in [2,7].

# 3. Identifying Violated Subtour Elimination Constraints

The problem of determining a violated subtour elimination constraint (2.6) is reduced (in the obvious way) to the same problem for the symmetric case. We outline here the procedure for completeness.

Let us assume that  $\overline{x}$  is the optimal solution of problem (2.1)-(2.5) where some constraints (2.6) and (2.8) may have been appended. We now consider the values  $\overline{x}_{i,j}$  as capacities of the arcs  $(i,j) \in A$ . The goal consists of identifying a node set  $W \subset V$  such that the related constraint (2.6) is a most violated SEC (if any). Then, the set W will be

$$W = \arg . \max \{ \bar{x}(A(W')) - |W'| + 1 > 0 \} \text{ for all } W' \subset V, 2 \le |W'| \le n - 1.$$
 (3.1)

For this purpose we will construct a symmetric directed graph, say  $\overline{D}$ , for which a certain mincut problem is to be solved. But, first, let us introduce additional notation. Let

$$\bar{y}_{v} = \bar{x}(\delta^{-}(v)) + \bar{x}(\delta^{+}(v)) \text{ for all } v \in V.$$
(3.2)

Next, we define an (auxiliary) digraph, say  $D_0 = (V_0, A_0)$ , as follows:

$$V_0 = V \cup \{0\}$$
, where 0 is a new node, (3.3)

$$A_0 = A \bigcup \{ (0,v) \mid v \in V \} \bigcup \{ (j,i) \mid (i,j) \in A \text{ and } (j,i) \notin A \}.$$

(That is, we add to V a node 0 (the "source"), and we add to A arcs (0,v) for all  $v \in V$  and all reverse arcs but do not create parallel arcs). Let us define the following capacities for  $D_0$ 

$$c_{0,v}^0 := 1 - \frac{1}{2} \, \overline{y}_v \text{ for all } v \in V$$
 (3.4)

(Note that  $c_{0,v}^o \ge 0$  for all  $v \in V$ , where  $c_{0,v}^o = 0$  whenever the related constraints (2.3) and (2.4) are satisfied as equalities). Further, we set

$$c_{i,j}^{0} = c_{j,i}^{0} = \frac{1}{2} \left( \overline{x}_{i,j} + \overline{x}_{j,i} \right) \text{ for all } (i,j) \in A_0 \setminus \{(0,v) | v \in V\}$$
(3.5)

(If  $(i,j)\notin A$  (resp.,  $(j,i)\notin A$ ) we set  $\overline{x}_{i,j}:=0$  (resp.,  $\overline{x}_{j,i}:=0$ ).)

Let  $\delta_0(W)$  denote the minimum capacity cut in  $D_0 = (V_0 A_0)$  with respect to the capacities (3.4) and (3.5), such that  $0 \notin W$ , and let  $c(\delta_0(W))$  denote the associated capacity. It follows from our construction that if

$$c(\delta_0^-(W)) \ge 1 \tag{3.6}$$

is satisfied then there is no SEC violated by the current LP solution  $\bar{x}$ . Otherwise, the SEC induced by W is a most violated constraint (2.6).

We now construct a symmetric directed graph, say  $\overline{D}$ : =  $(V_0, \overline{A})$ , related to digraph  $D_0 = (V_0, A_0)$ , by setting

$$\overline{A}:=A_0 \bigcup \{(v,0)|v\in V\} \tag{3.7}$$

with capacities

$$\bar{c}_{i,j} = \bar{c}_{i,j} = c_{i,j}^0 = c_{i,j}^0 \text{ for all } (i,j) \in A_0$$
 (3.8)

$$\overline{c}_{v,0} := c_{0,v}^0 \text{ for all } v \in V \tag{3.9}$$

 $(\overline{D}$  is a symmetric digraph since if any two nodes, say u and v are linked by an arc then both arcs (u,v) and (v,u) are in the digraph). The underlying undirected graph  $G=(V_0,E)$  of  $\overline{D}=(V_0,\overline{A})$  is defined by

$$E = \{ ij \mid (i,j) \in \overline{A} \text{ and } (j,i) \in \overline{A} \}$$
 (3.10)

with capacities

$$\hat{c}_{ij} := \overline{c}_{i,j} = \overline{c}_{j,i} \text{ for all } ij \in E$$
(3.11)

It has the property that for each  $W \subset V$ ,

$$\hat{c}(\delta(W)) = c(\delta_0^-(W)) \tag{3.12}$$

where  $\delta(W) = \{ ij \in E | i \in V \setminus W, j \in W \}.$ 

Thus, a minimum capacity cut  $\delta(W)$  in G corresponds to a minimum capacity cut  $\delta_{\bar{o}}(W)$  and vice versa. Such a cut  $\delta(W)$  can be obtained with the Gomory-Hu procedure. Padberg and Rinaldi [15] describe a practically efficient version of this procedure.

The main loop of the algorithm requires at most n major iterations. At any major iteration two nodes at least are "contracted" or "shrunk" into a a single node. Two main steps are executed at each major iteration: first, a series of tests are designed to shrink as many nodes as possible; and, second, a max-flow algorithm is executed by selecting any two nodes of the shrunk graph as nodes "source" and "sink". The algorithm described by Goldberg and Tarjan [9] is used in the second step; its complexity is  $p(n) = O(mn \log(n^2/m))$ , where m = |E|. The tests that are performed in the first step have a complexity not worse than p(n). Then, the overall complexity is  $O(n^4)$ . Although this is not better than the complexity of other published algorithms, the "shrinking" mechanism suggests a better efficiency in practice; this is confirmed by extensive computational experience reported in [16,17]. Since the algorithm obtains successively a series of better cuts till an optimal one is reached, we may generate the related induced SECs if they are violated, instead of using only a 'best' one.

## 4. Identifying Violated Precedence Forcing Constraints

We now describe how one can decide whether a given point satisfies all precedence forcing constraints, and if it does not, how one can find a (most) violated PFC (2.8). Let us assume that a vector  $\bar{x}$ , for practical purposes the current LP-solution, is given. For every  $(j,i) \in R$  we do the following. Construct a new digraph  $D_{j,i} = (V_{j,i}, A_{j,i})$  from D = (V,A) by deleting some arcs (those which never appear in (2.8)) and by shrinking the nodes i and j. Formally, set

$$V_{j,i} = V \setminus \{i,j\} \cup \{v_{j,i}\} \tag{4.1}$$

where  $v_{ij}$  is a new 'special' node representing j and i, and

$$A_{j,i} = \{ (k,l) \mid (k,l) \in A, k \notin \{i,j\}, l \notin \{i,j\} \}$$

$$\bigcup \{ (v_{j,i},l) \mid (j,l) \in A, l \notin \{i,j\} \}$$

$$\bigcup \{ (k, v_{i,j}) \mid (k,i) \in A, k \notin \{i,j\} \}$$

$$(4.2)$$

Figure 1 shows the digraph  $D_{j,i}$  induced by shrinking the nodes i and j in digraph D. Note that we throw out every arc directed into j and every arc directed from i; by definition,  $(j,i)\notin A$ . Every arc  $a\in A_{j,i}$  gets a capacity  $\hat{X}_a$  that is nothing but the value  $\overline{X}_a$  of the corresponding arc a in D=(V,A). Note that a PFC (2.8) for the old digraph D can be written as

$$x(A_{j,j}(\hat{W})) \leq |\hat{W}| - 1$$
 (4.3)

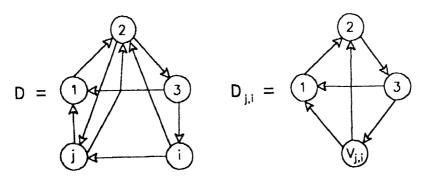


Figure 1. Original and shrinking graphs of precedence relationships

with respect to the new digraph  $D_{\mu\nu}$ , where  $\hat{W}=W\bigcup\{v_{\mu\nu}\}$ .

Our goal is to find a set W in  $D_{j}$ , with  $v_{j}$ ,  $\in W$  and  $W \geq 2$  such that the related constraint (4.3) forcing the precedence relation (i,j) is a most violated one or to show that no violated constraint of that type exists. To do this, we create an auxiliary digraph  $D_0$  from  $D_{j}$ , (exactly in the same way as  $D_0$  was constructed from D in Section 3) and determine a minimum capacity cut in  $D_0$  that separates 0 from  $v_{j,k}$ . This can be done by applying any max-flow algorithm. Suppose  $\delta_0(W)$  is such a cut with  $0\notin W$  and  $v_{j,k}$ . W, and let  $\gamma = c^{\gamma} c(\delta_0(W))$  be its capacity. One can show that the following holds.

If y > 1 then no PFC related to (ij) is violated by  $\tilde{x}$ . If y = 1 then  $\hat{W} \ge 2$  and letting  $W' := \hat{W} \setminus \{v_{ij}\}$  the PFC

$$x((j:W^*)) + x(A(W^*)) + x((W^*:i)) \le |W^*|$$
(4.4)

is violated by  $\vec{x}$ , in fact, this is a most violated constraint forcing the precedence relation (ij).

For obtaining a minimum capacity cut  $\delta_0(\hat{W})$  separating 0 from  $v_D$  we use the algorithm described in [9]; its complexity is  $O(n^3)$ .

For illustrative purposes let us consider the complete digraph  $D_n = (V, A_n)$  for n = 7 and the matrix C shown in Table 1. The set R will be  $R = \{(1j)\} \cup \{(i,5)\}$  for j = 4,5,6,7 and i = 1,4,6,7. Our inexact algorithm [4] gives the feasible solution  $1 \rightarrow 4 \rightarrow 2 \rightarrow 7 \rightarrow 6 \rightarrow 5 \rightarrow 3$ . The value of the objective function is  $\overline{z} = 21.25$ . The LPAP optimal value is  $\underline{z} = 18.00$  and, then, there is a 14.11% gap. The current implementation of our (exact) algorithm adds 8 cuts. The strongest LP lower bound is  $\underline{z} = 20.75$  (then the gap is 2.29%). A branch-and-cut phase requires two nodes to prove the optimality of the initial solution. We currently have a PC and a mainframe implementation of the algorithm described above. The PC-version solves problems up to n = 100 nodes and r = 280 precedence relationships in less than two and a half CPU hours. The mainframe version does this in a few seconds.

\n2 \n1\	1	2	3	4	5	6	7
1 2 3 4 5 6 7	4.00 7.00 2.75 0.00 10.00 4.00	1.00 - 8.00 2.50 1.00 11.00	2.00 5.00 - 2.25 2.00 12.00 5.00	5.50 - 0.75 10.75	0.00 4.00 7.00 2.75 - 10.00 4.00	3.00 6.00 9.25 3.00 6.00	1.00 0.00 8.00 2.50 1.00 11.00

Table 1. Matrix C

## **Conclusions**

In this work we have presented a 0-1 model for the Sequential Ordering Problem. It is stronger than the model introduced in [4,5]. We have also outlined an (exact) LP-based algorithm. An extensive computational study is in progress. More theoretical work is required mainly for identifying in reasonable polynomial time violated lifted SECs and PFCs. In any case, we believe that the LP-based approach is a most promising way to get strong lower bounds on optimal solutions for this type of problems.

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